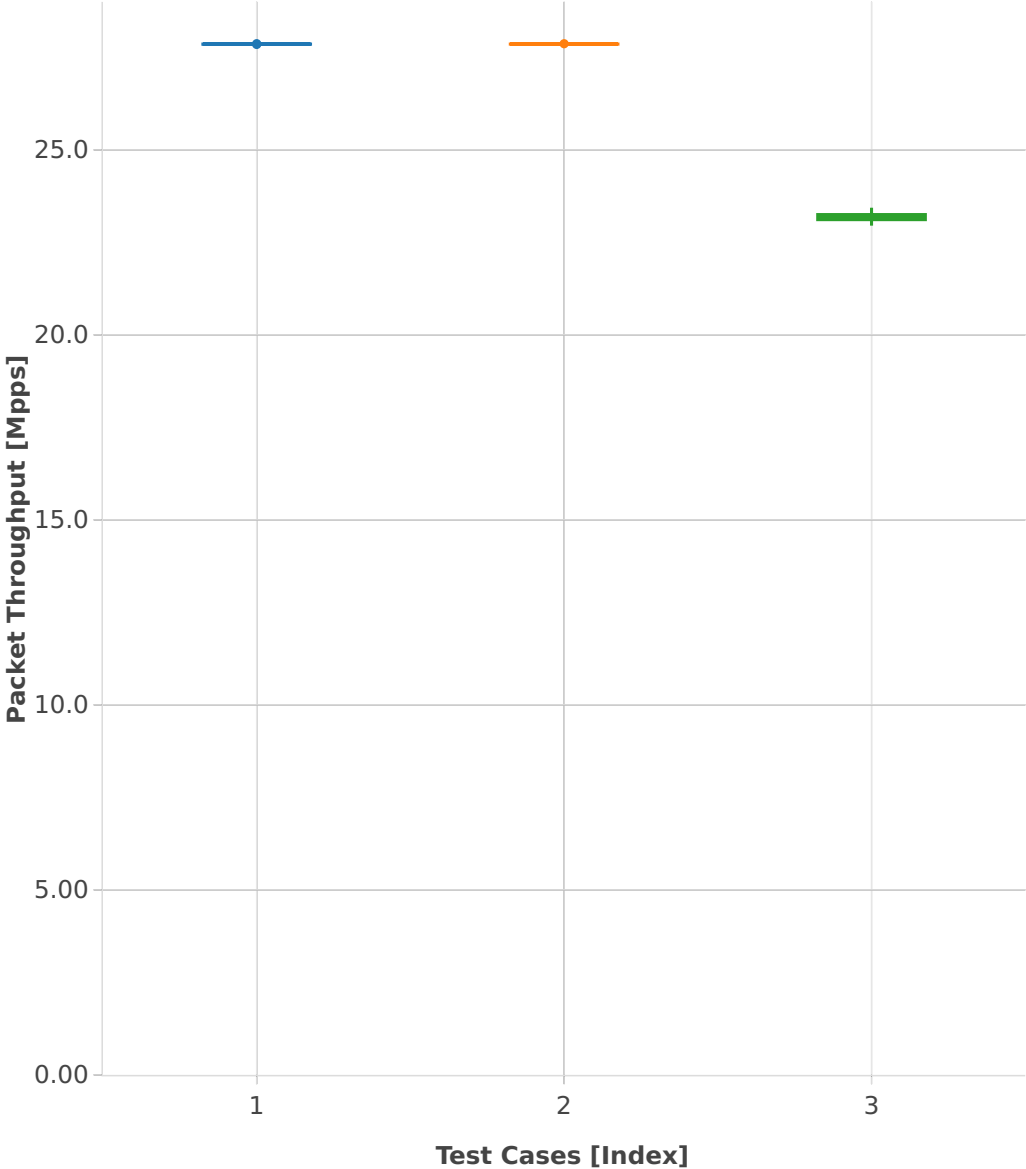


**Throughput: l2sw-3n-hsw-xl710-64b-2t2c-base-ndr**



- 1. (10 runs) eth-l2patch
- 2. (10 runs) eth-l2xcbase
- 3. (10 runs) eth-l2bdbasemaclrn