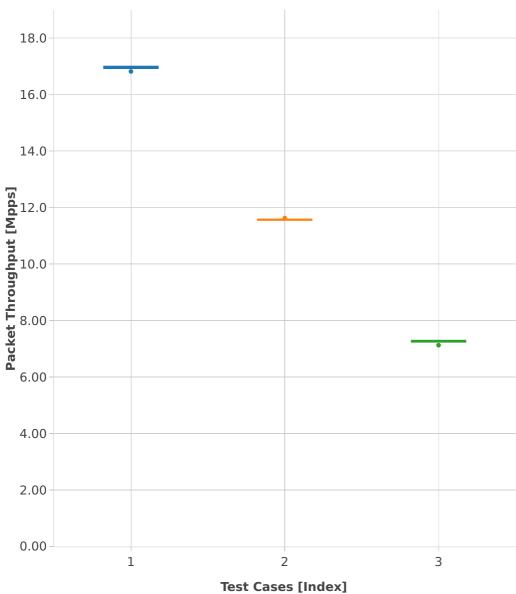
## **Throughput:** I2sw-3n-hsw-x520-64b-1t1c-base\_and\_scale-pdr



- 1. (10 runs) eth-I2xcbase
- 2. (10 runs) eth-l2bdbasemaclrn
- 3. (10 runs) eth-l2bdscale1mmaclrn