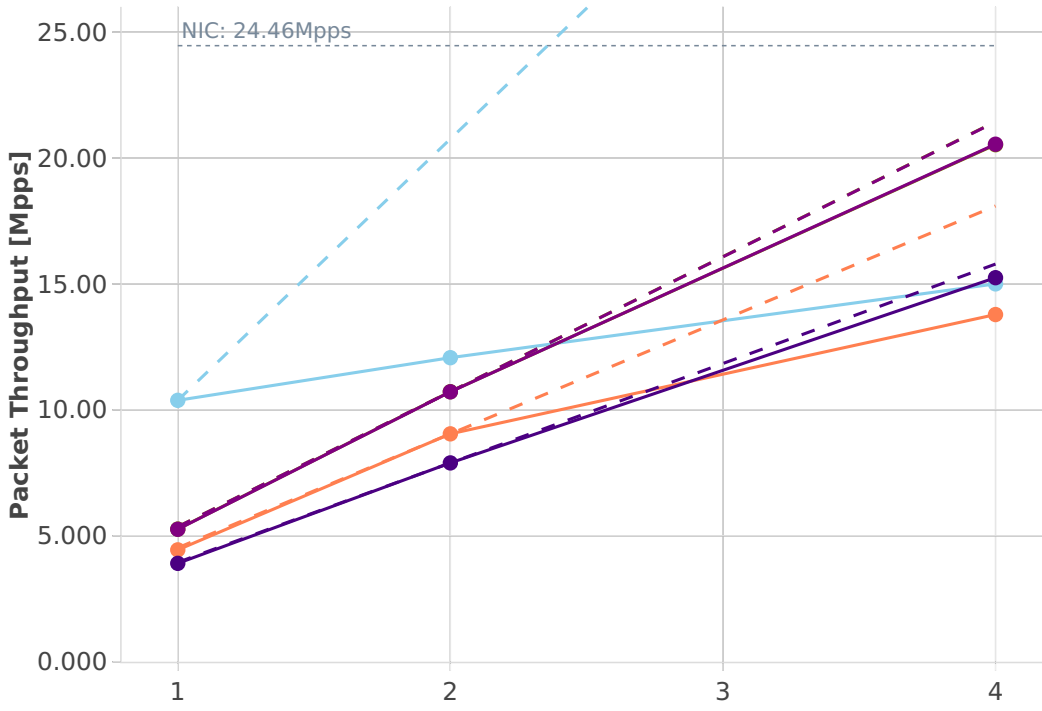


Speedup Multi-core: memif-3n-hsw-x520-64b-base_and_scale-pdr



-- Perfect — Measured ··· Limit

- eth-l2xcbase-eth-1memif-1dcr
- eth-l2xcbase-eth-2memif-1dcr
- eth-l2xcbase-eth-2memif-1lxc
- eth-l2bdbasemaclrn-eth-2memif-1lxc
- dot1q-l2bdbasemaclrn-eth-2memif-1dcr