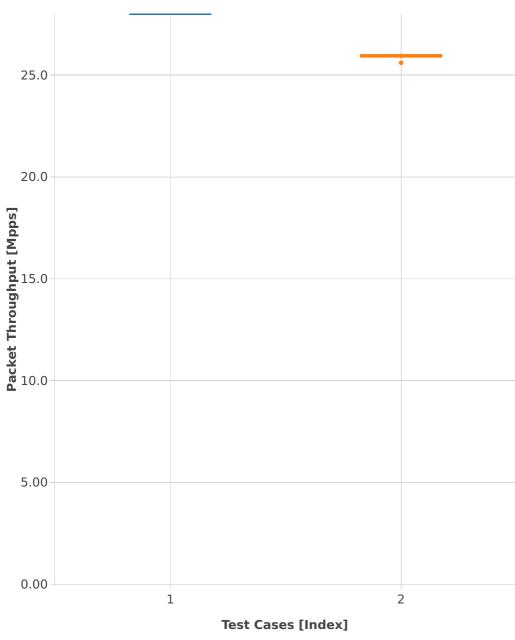
## **Throughput:** I2sw-3n-hsw-xI710-64b-2t2c-base\_and\_scale-pdr



■ 1. (10 runs) eth-l2patch ■ 2. (10 runs) eth-l2bdbasemacIrn