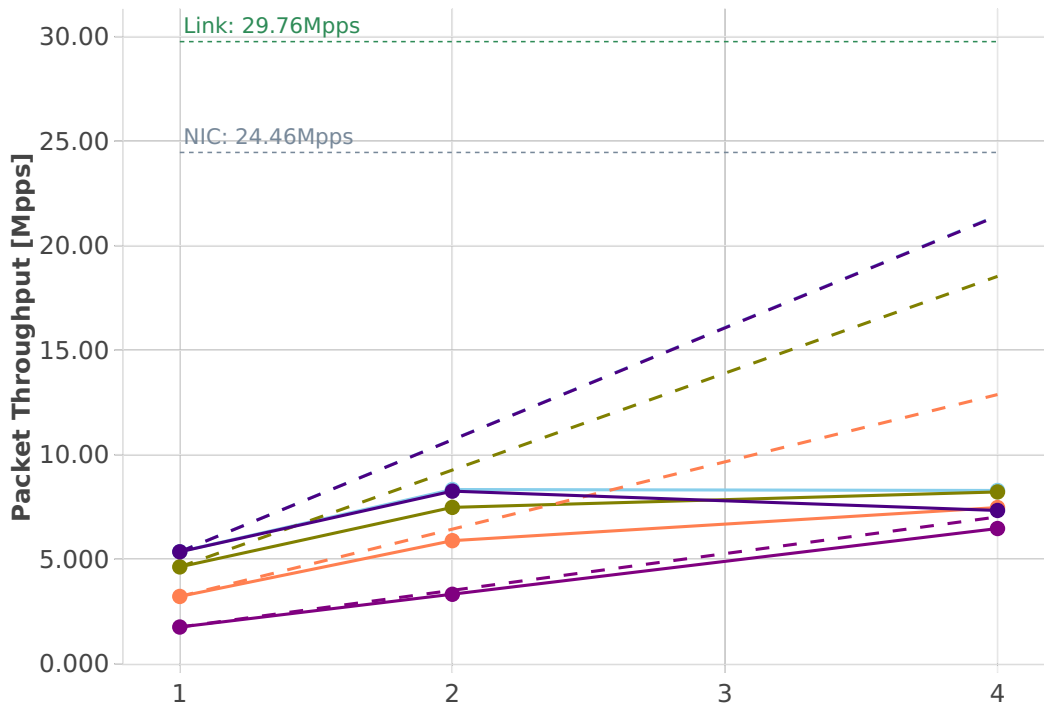


Speedup Multi-core: k8s-memif-3n-hsw-x520-64b-base_and_scale-l2xc-pdr



- Perfect — Measured ... Limit
- eth-1drcl2xcbase-eth-2memif-1drcl2xc-1paral-k8s
 - eth-1drcl2xcbase-eth-2memif-2drcl2xc-1horiz-k8s
 - eth-1drcl2xcbase-eth-8memif-4drcl2xc-1chain-k8s
 - eth-1drcl2xcbase-eth-4memif-2drcl2xc-1chain-k8s
 - eth-1drcl2xcbase-eth-2memif-4drcl2xc-1horiz-k8s